

Freeform Search

Database:	<div style="border: 1px solid black; padding: 2px;"> US Pre-Grant Publication Full-Text Database US Patents Full-Text Database US OCR Full-Text Database EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index IBM Technical Disclosure Bulletins </div>
Term:	<div style="border: 1px solid black; padding: 2px;"> 6625673.pn. </div>
Display:	10 Documents in Display Format: - Starting with Number 1
Generate: <input type="radio"/> Hit List <input checked="" type="radio"/> Hit Count <input type="radio"/> Side by Side <input type="radio"/> Image	

Search
Clear
Interrupt

Search History

DATE: Sunday, January 07, 2007
 [Purge Queries](#)
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<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
	DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ		
<u>L20</u>	6625673.pn.	2	<u>L20</u>
<u>L19</u>	6526459.pn.	2	<u>L19</u>
<u>L18</u>	(expand or expanding) same memory same l17	18	<u>L18</u>
<u>L17</u>	(finite or fixed) adj3 ((bus adj2 width) or address or bus)	8733	<u>L17</u>
<u>L16</u>	(expand or expanding) same memory same l15	0	<u>L16</u>
<u>L15</u>	(finite or fixed) adj3 (bus adj2 width)	57	<u>L15</u>
<u>L14</u>	(expand or expanding) same memory same (address adj3 bus adj3 width)	4	<u>L14</u>
<u>L13</u>	L12 and l5	0	<u>L13</u>
<u>L12</u>	(translate or translating) same address same (expand or expanding) same memory	39	<u>L12</u>
<u>L11</u>	(expand or expanding) same (address or addressing) same memory same (bus adj2 width)	22	<u>L11</u>
<u>L10</u>	(expand or expanding) same memory same (bus adj2 width)	54	<u>L10</u>
<u>L9</u>	L8 and l7	2	<u>L9</u>
<u>L8</u>	711/2.ccls.	343	<u>L8</u>

<u>L7</u>	L6 same l5	100	<u>L7</u>
<u>L6</u>	(access or accessing) same memory same (bus adj2 width)	1385	<u>L6</u>
<u>L5</u>	memory same (bus adj2 width) same (limit or limited or limitation)	637	<u>L5</u>
<u>L4</u>	memory same (address adj2 range) same (bus adj2 width) same (limit or limited or limitation)	0	<u>L4</u>
<u>L3</u>	(limit or limitation) same (bus adj2 width) same memory same (address adj2 range)	0	<u>L3</u>
<u>L2</u>	(limit or limitation) same (bus adj2 width) same (access or accessing) same (address adj2 range)	0	<u>L2</u>
<u>L1</u>	(limit or limitation) same (bus adj2 width) same (access or accessing) same above same (address adj2 range)	0	<u>L1</u>

END OF SEARCH HISTORY